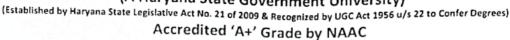


J.C. Bose University of Science and Technology, YMCA, Faridabad

(A Haryana State Government University)





Department of Electronics Engineering

Date: 23.08.2024

Subject: Physical Counseling of M.Tech (ECE / VLSI Design)

The Physical Counseling of M.Tech (Electronics and Communication Engineering) and M.Tech (VLSI Design) is scheduled to held from 27.08.2024 to 30.08.2024.

Venue: Control System Lab (1st Floor, Department of Electronics Engineering)

Reporting Time: 09:30 A.M

Tentative Vacant seats are as follows:

M.Tech (Electronics and Communication Engineering):

| General | Supernumerary seats | | | |
|---------|---------------------|--------|------------------|--|
| | EWS | Orphan | Kashmiri Migrant | |
| 17 | 1 | 1 | 1 | |

M.Tech(VLSI Design):

| General | | Supernumerary seats | | | |
|---------|-----|---------------------|-----|------------------|--|
| | EWS | Orphan | TFW | Kashmiri Migrant | |
| 16 | 1 | 1 | 1 | 1 | |

It is further informed that candidates are invited for the said admissions by collecting the application form physically only on same day of counseling.

Chairperson

(Electronics Engineering)

Deptt. of Electronics Engineering J.C. Bose University of Science and Technology, YMCA, Faridabad