



**J.C. Bose University of Science and Technology, YMCA, Faridabad**  
**(A Haryana State Government University)**  
(Established by Haryana State Legislative Act No. 21 of 2009 & Recognized by UGC Act 1956 u/s 22 to Confer Degrees)  
**Accredited 'A+' Grade by NAAC**



**Department of Electronics Engineering**

**Date :11.09.2024**

**Subject : Physical Counseling of M.Tech (ECE / VLSI Design)**

The Physical Counseling of M.Tech (Electronics and Communication Engineering) and M.Tech (VLSI Design) is scheduled to held on 13.09.2024.

**Venue : Control System Lab (1<sup>st</sup> Floor, Department of Electronics Engineering)**

**Reporting Time : 09:30 A.M**

Tentative Vacant seats are as follows:

**M.Tech (Electronics and Communication Engineering) :**

General	Supernumerary seats		
	EWS	TFW	Kashmiri Migrant
17	2	0	1

**M.Tech(VLSI Design) :**

General	Supernumerary seats		
	EWS	TFW	Kashmiri Migrant
17	2	1	1

*Y/E*  
*11/9/24*  
Chairperson

Chairperson  
(Electronics Engineering)  
Deptt. of Electronics Engineering  
J.C. Bose University of Science  
and Technology, YMCA, Faridabad