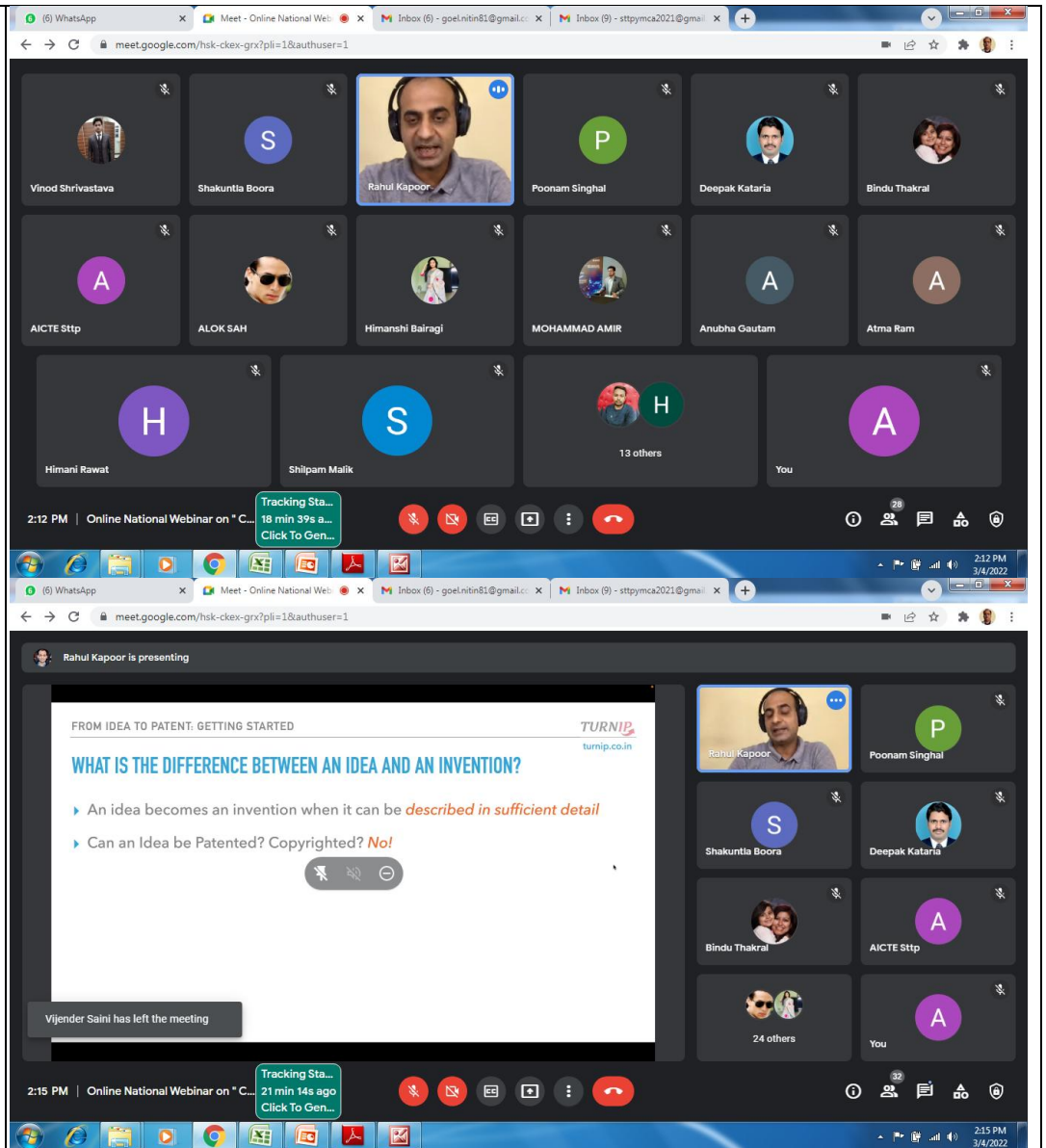


## Activity Report

Online National Webinar on "**Converting Research to Patents**" on 4<sup>th</sup> March 2022

Digital Platform with Participation Link	Google Meet joining info Video call link: <a href="https://meet.google.com/hsk-ckex-grx">https://meet.google.com/hsk-ckex-grx</a>
Date and Time of the webinar	4 <sup>th</sup> March 2022, 02:00 PM to 03:00 PM
Participants' Domain	Faculty, Research Scholars, UG & PG Students
Organizing department	Department of Electrical Engineering, J C Bose University of Science and Technology, YMCA, Faridabad
Webinar Coordinator with Designation	Dr. Shakuntla & Mr. Nitin Goel Assistant Professor (Electrical Engineering) JCBUST, YMCA, Faridabad
Keynote Speaker/Speaker(s)	<b>Dr. Rahul Kapoor</b> Founder and Director of Turnip Innovations
Objective of the webinar	To aware the participants about to Patents, Importance of Patents, and patent writing process.
Accomplishment(s) summary	The webinar was organised in a successful manner. Though more than 50 participants from Central/State Universities and organizations of different states have attended the webinar. The speaker addressed the participants with lots of information regarding the IPR, Patents, Importance of Patents, requirements of patentability, Application Preparation and Filing etc.
Webinar Flyer	 <p>The flyer is a yellow and white graphic with the following content:</p> <ul style="list-style-type: none"> <li><b>Header:</b> J.C. BOSE UNIVERSITY OF SCIENCE AND TECHNOLOGY, YMCA, FARIDABAD (NAAC 'A' Grade Haryana State Government University)</li> <li><b>Resource Person:</b> RAHUL KAPOOR, Ph.D, Founder, Turnip Innovations, Web: turnip.co.in</li> <li><b>Programme Chair:</b> Dr Poonam Singhal, Chairperson, Department of Electrical Engg. J.C. Bose UST, YMCA</li> <li><b>Coordinators:</b> Dr. Shakuntla (8076416816)   Mr. Nitin Goel (9313241442) Assistant Professor, Electrical Engg. Department</li> <li><b>Webinar Title:</b> "Converting Research to Patents" on 4<sup>th</sup> March, 2022</li> <li><b>Department:</b> DEPARTMENT OF ELECTRICAL ENGINEERING, J.C. Bose University of Science and Technology, YMCA, Faridabad</li> <li><b>Patron:</b> Sh. Raj Nehru, Vice-Chancellor, J.C. Bose UST, YMCA</li> <li><b>Co-Patron:</b> Dr. Sunil Kumar Garg, Registrar, J.C. Bose UST, YMCA</li> <li><b>Registration Link:</b> <a href="https://docs.google.com/forms/d/e/1FAIpQLScGuLmdM_uillEdIxxGHrLBNNtjeBUJMaV56uNgeogly4cg/viewform?usp=pp_url">https://docs.google.com/forms/d/e/1FAIpQLScGuLmdM_uillEdIxxGHrLBNNtjeBUJMaV56uNgeogly4cg/viewform?usp=pp_url</a></li> <li><b>Important Dates:</b> Last date for registration: 03-03-2022</li> <li><b>Footer:</b> <a href="http://jboseust.ac.in">jboseust.ac.in</a> /JCBoseUST</li> </ul>

Webinar delivery  
Image(s)



Sample Certificate

