# Rati rate Hyper

#### J.C. Bose University of Science & Technology YMCA, Faridabad

(A Haryana State Government University)

(Established by Haryana State Legislative Act No. 21 of 2009 & Recognized by UGC Act 1956 u/s 22 to Confer Degrees)

Accredited 'A' Grade by NAAC

#### **Department of Electronics Engineering**

#### **ACTIVITY REPORT**

#### Name of Activity:Lecture Series 3

Nameofdepartmentconductingtheactivity	ElectronicsEngineering
Dateofconduct	20/05/2022
Days of Activity	1 day
Nature of activity	Offline
Funded/ Sponsored by from(University/Industry/UGC/AICTE/DST/TEQIP/Out sideSociety/agency/others(mention)(ifany)	Dept of Electronics Engg, J.C Bose University, YMCA, Faridabad
ActivityCoordinator/s	Dr. NitinSachdeva Ms. SangeetaDhull
Total AmountSpent	NIL
Targetaudience	Faculty Members, Research Scholars, Workshop Staff and PG Students of the Electronics department
Total No. of Registered Participants	NA
No. of Registered Participants from Other Institutions	Nil
No. of Registered Participants from Other Country	Nil
No.ofParticipants completing the program	21
No. of Expert/s during the program	01
Name of Expert/s	Prof. MunishVashishath

Link of the Activity on website	https://jcboseust.ac.in/electronics/index.php/ge neral-notice/165-seminar-on-interfacing-of- microprocessors-with-peripheral-devices		
Video Link of the Activity( if any)	Nil		
Detailed Activity Report	The lecture was delivered by Dr. Munish		
	Vashishath on topic "Trends in Microprocessors		
	and Microcontrollers". He has delivered his talk		
	successfully by exploring Microprocessors and		
	their interfacing with one or more peripheral		
	devices for the purpose of communication with		
	various input and output devices connected to it.		
	Interface is the path for communication between		
	two components. Interfacing is of two types,		
	memory interfacing and I/O interfacing.During		
	the early days of the microprocessor revolution,		
	these techniques required complex hardware		
	consisting of Medium scale integrationdevices		
	making the design highly complex and time		
	consuming. So, the manufacturers(INTEL) have		
	developed a large number of general and special		
	purpose peripheraldevices most of them being		
	single chip circuits. They are also programmable		
	devices.Microprocessors are being used in a wide		
	range of communication equipment. In		
	telephoneIndustry, these are used in digital		
	telephone sets. Telephone exchanges and modem		
	etc. The useof microprocessor in television,		
	satellite communication has made		
	teleconferencing possible.Railway reservation and		
	air reservation system also uses this technology.		
	LAN and WAN forcommunication of vertical		
	information through computer network.		

Attach Brochure of the Activity	Attached in Appendix 1
Attachgoodqualityphotographs (with captions)	Attached in Appendix 2
Sample Certificate of the Activity	The certificate of appreciation has been given to the speaker.The certificate is attached in Appendix 3

#### **Appendix 1**



Appendix 2 The 80186/80188 double the performance of 8086 80186/80188 = 8086/8088 + several additional chipsClock generator Two independent high-speed DMA channels Programmable interrupt controller Three programmable 16-bit timers Programmable wait state generator Local bus controller Programmable memory and peripheral chip-select logic The total addressable memory size is 1MB. The instruction set is upward code comp 8086/88 family.







#### **Appendix 3**



#### J.C. Bose University of Science and Technology, YMCA, Faridabad

(A Haryana State Government University)
Accredited 'A' Grade by NAAC

NH-2, Sector-6, Mathura Road, Faridabad, 121006 (Haryana)

Electronics Engineering Department - Seminar Series



#### Certificate of Appreciation

This certificate is presented to Dr./Mr./Ms. Munish. Yashishath. for sharing his/her valuable knowledge as an Expert on the topic Trends in Microprocessors & Microcontrollers in the Seminar Series organized by Department of Electronics Engineering on 20th May, 2022.

Coordinators

Electronics Engineering Department - Seminar Series

Chairperson

Electronics Engineering Department

## J.C. Bose University of Science & Technology, YMCA, Faridabad

### Expert Lecture Series 3

Speaker of the day: Dr. Munish Vashishath

Date: 20/05/2022

S.No.	Name	Faculty/Workshop Staff/ Research Scholar/ PG student	Signature
1	Phone Aharton	faculty Pt/ Tagent	"
2	Necta Gula	faculty	Memple
3	Heelem Turk	faculty	Ter Com
4	Parini Chawla	Faculty	Vi
5.	Marja Kumari	Faculty	Visit !
6.	Gurjas Sandara		guy
7	Reema	Pho scholer	Sougat
8	Sangeote Dhall	faculty	- Political
9.	Sonam Khuka	& faculty	Juliu .
19	Kalpara Sheo bus	faculty	100
11.	Bal Krishen	Faculty	TO
/2	Dishint Shuleto	faculty U	T
13	Produp Kund	Gully	14/2
14	Latit Ren	foully	02-
15	Bashard Kuran	faculty	Sit
16.	Amir Mansoon	PhD Scholar	Aune
17	Sandrep	PhD Scholor	D.M.
18-	Protible	Ph.D. Scholay	-Balible
19	Netwi-Sa chole un	facuoty	TAN
26	Archae Agaml	faculty'	Johns
21	Vinad Rather	faculty	1/2_