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J.C. Bose University of Science & Technology YMCA, Faridabad

(A Haryana State Government University)

(Established by Haryana State Legislative Act No. 21 of 2009 & Recognized by UGC Act 1956 u/s 22 to Confer Degrees)

Accredited 'A' Grade by NAAC

Department of Electronics Engineering

ACTIVITY REPORT

Name of Activity:Lecture Series 5

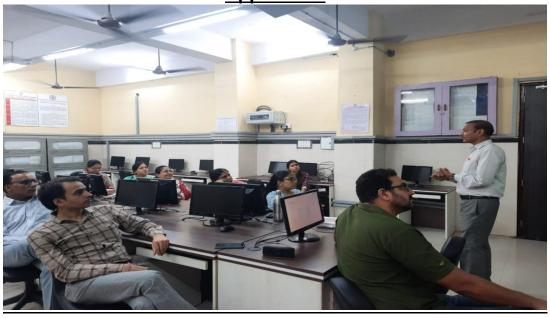
| Name of department conducting the activity | Electronics Engineering |
|---|--|
| Dateofconduct | 09/09/2022 |
| Days of Activity | 1 day |
| Nature of activity | Offline |
| Funded/ Sponsored by from(University/Industry/UGC/AICTE/DST/TEQIP/Out sideSociety/agency/others(mention)(ifany) | Department of Electronics Engineering, J.C Bose University of Science & Technology, YMCA, Faridabad |
| ActivityCoordinator/s | Dr. Nitin Sachdeva Ms. Sangeeta Dhall |
| Total AmountSpent | NIL |
| Targetaudience | Faculty Members, Research Scholars, Workshop Staff and PG Students of the Electronics department |
| Total No. of Registered Participants | NA |
| No. of Registered Participants from Other Institutions | Nil |
| No. of Registered Participants from Other Country | Nil |
| No.ofParticipants completing the program | 24 |
| No. of Expert/s during the program | 01 |
| Name of Expert/s | Mr. Bharat Bhushan |

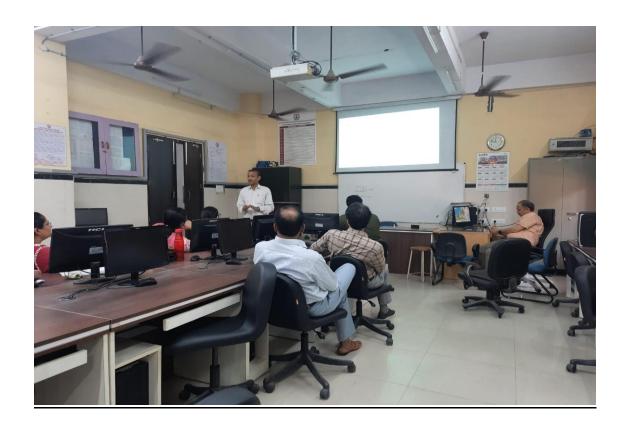
| Link of the Activity on website | https://jcboseust.ac.in/electronics/index.php/notice-2/174-seminar-on-advance-concept-in- |
|--|--|
| | digital-design |
| Video Link of the Activity(if any) | Nil |
| Detailed Activity Report | The lecture was delivered by Mr. Bharat |
| | Bhushanon topic "Advance Concepts in Digital |
| | Design". He has delivered his talk successfully by |
| | exploring the concepts of digital design. Digital |
| | Logic is rooted in binary code, a series of zeroes |
| | and ones each having an opposite value.At the |
| | base of all electronic circuit developments, |
| | fighting to make our world more comfortable, |
| | digitized and "gadgety", digital circuits play a |
| | huge role. These processes require massive digital |
| | data processing. At the end all these processes are |
| | working towards making the signal ON or |
| | OFF.Digital Logic Design is all about creating |
| | this hardware, such as Circuit boards, ICs, |
| | Memories and Microchip processors. |
| | The faculty members, workshop staff, research |
| | scholars and PG students of the electronics |
| | department have attended this lecture. |
| Attach Brochure of the Activity | Attached in Appendix 1 |
| Attachgoodqualityphotographs (with captions) | Attached in Appendix 2 |
| Sample Certificate of the Activity | The certificate of appreciation has been given to the speaker. The certificate is attached in Appendix 3 |

Appendix 1



Appendix 2







Appendix 3



J.C. Bose University of Science & Technology, YMCA, Faridabad Expert Lecture Series 5

Speaker of the day: Mr. Bharat Bhushan

Date: 09/09/2022

| .No. | Name | Designation | Signature |
|-------|--------------------------|-----------------|---------------------|
| 1. | PROF.MUNISH VASHISHATH | PROFESSOR | West Heater |
| 2. | PROF. PRADEEP KUMAR | PROFESSOR | Action 19/12 |
| 3. | PROF. NEELAM TURK | PROFESSOR | Helder |
| 4. | DR.SHAILENDER GUPTA | ASSOCIATE PROF. | gal |
| 5. | SH.BHARAT BHUSHAN | ASSISTANT PROF. | 14-4, 0 |
| 6. | MS.SANGEETA DHALL | ASSISTANT PROF. | sauget to |
| 7. | THE REAL WAR | ASSISTANTPROF. | pangellata on leave |
| 8 | The state of the | ASSISTANT PROF. | P. Dr. |
| 9 | DR.SHEILZA JAIN | ASSISTANT PROF. | Market . |
| 10 | - ISTAL KINSTIAN | ASSISTANT PROF. | BU 09/9/22 |
| 11 | . SH.LALIT RAI | ASSISTANT PROF. | 0 |
| 12. | Dr .DUSHYANT SHUKLA | ASSISTANT PROF. | Mala Jazz |
| 13. | MS.ARCHANA AGGARWAL | ASSISTANT PROF. | 10184-102 |
| 14. | MS. ARCHANA AGGARWAL(EL) | ASSISTANT PROF. | Acceptation |
| 15. | Dr.NITIN SACHDEVA | ASSISTANT PROF. | Van Glazza |
| 16. | Dr .PRIYANKA | ASSISTANT PROF. | Miggire |
| 17. | SH.VINOD RATHORE | ASSISTANT PROF | -on leave - |
| 18. | Dr .PRASHANT KUMAR | ASSISTANT PROF. | 90 |
| 19. | Dr .SONAM KHATTER | ASSISTANT PROF. | War. |
| 20. | Dr .RASHMI CHAWLA | ASSISTANT PROF. | Almaja/2 |
| 21. / | MS.GUNJAN SARDANA | ASSISTANT PROF. | anjer |
| | | | Ja/a/22 |

| | | ASSISTANT PROF. | (XS) |
|------------|--|-------------------|---|
| | Dr.KALPANA SHEOKAND | ASSISTANT PROF. | Mary |
| 22. | MS.MANJU KUMARI | ASSISTANT PROF. | & Comments of the Comments of |
| 23. | Dr. SUNIL JADAV | ASSISTANT PROF. | C. S. |
| 24. | Dr. ROHIT | ASSISTANT PROF. | N. |
| 25. 26. | MS. NISHA YADAV | HOS, ECE | |
| 77. | Mr. LALIT MOHAN | HOS, W/S | |
| | r. DHARAMVIR | Senior Instructor | |
| | IS KUSUM ARORA | Senior Instructor | |
| | r MUKESH KUMAR | Instructor | |
| - | s.POULAMI | Instructor | |
| - | Ir. VIJAY KUMAR | Technician | |
| | Ar RAMAVTAR | Instructor | |
| | MIS DIPIKA | Instructor | |
| 35. | Mr. VARINDER SINGH | Technician | |
| 36. | Mr. VIRENDER SINGH | Lab Assistant | |
| 37. | Mr. BAIJNATH | Technician | |
| 39. | | Ph.D Scholar | |
| 40. | PRATIBHA RANI | Ph.D Scholar | Entiby |
| 41. | SURINDER SINGH | Ph.D Scholar | |
| 42. | POOJA | Ph.D Scholar | |
| 43. | SANDEEP | Ph.D Scholar | Sup 18 |
| 44. | SHWETA | Ph.D Scholar | tuy 100 |
| 45. | - | Ph.D Scholar | Anjale. |
| 46. | NISHA YADAV | Ph.D Scholar | |
| 47. | DIKSHANT SHARMA | Ph.D Scholar | Dikehant |
| 48. | NEHA VERMA | Ph.D Scholar | |
| 49. | REEMA | Ph.D Scholar | |
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