

LECTURE PLAN

	Sub:- AVLSI	Semester	II
	Code: E16V 602	Faculty Name: Sunil Jadav	
	JAN- MAY 2018		
Sr.No	Topic to be covered	No. of Lectures required	Mode of Lecture
1	Introduction to course	1	
2	Small signal and Large signal Models of BJT and MOSFET	2	White BOARD
3	Analog MOS Process	1	White BOARD
4	Single Stage Amplifiers CS,CD,CG	3	White BOARD
5	Multi-Stage Transistor Amplifier, CC-CE,CC-CC	3	White BOARD
6	Cascode configuration	1	White BOARD
7	Differential Amplifier: Differential pair and DC transfer characteristic	3	White BOARD
8	Current Mirrors	3	White BOARD
9	Current & Voltage reference Circuits	2	White BOARD
10	Analysis of Differential Amplifier with active loads	3	White BOARD
11	Frequency response analysis	2	White BOARD
12	Operational Ampfier Theory and Design	2	White BOARD
13	Design of two stage MOS Operational Amplifier	1	White BOARD
14	Design of two stage MOS Operational Amplifier with cascodes	1	White BOARD
15	MOS telescopic cascode operational amplifiers	1	White BOARD
16	MOS folded-cascode operational amplifier	1	White BOARD
17	Digital to Analog and Analog to Digital converters	4	White BOARD
18	Voltage controlled oscillators	1	PPT
19	Comparators	2	PPT
20	Phase Locked Loops	2	White BOARD
21	OTA and Switched Capacitors Circuits and Filters	3	PPT
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	Text Books: As per syllabus		