


Name	Dr. Prashant Kumar	
Designation	Associate Professor	
Date of Joining	15/08/2008	
Qualification	B.Tech (ECE) M.Tech(VLSI Design) Ph.D. (Semiconductor Devices)	
Area of Specialization	VLSI Design/Semiconductor Devices	
Teaching Experience	16 Years	
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THESIS/DISSERTATION/PROJECTSUPERVISIONEXPERIENCE

M.Tech.:26 Completed

B.Tech: 25 Completed

MEMBERSHIP IN PROFESSIONAL ASSOCIATIONS

- 1. Life Member ISTE(LM107745)**
- 2. Member of IEI**
- 3. Life Member NTSI (LM426)**

PUBLICATIONS

International/National Journals:

International Journal

1. Pinki Rani, Prashant Kumar, Munish Vashishath, "An Efficient Approach to Design 5 Bit Flash ADC" YMCAUST International Journal of Research, Vol 1, Issue II, July 2013 page 50-57.
2. Neeraj Gupta, Rashmi Sharma, Prashant Kumar, "Comparative study of Low pass filter using OPAMP and current conveyor" YMCAUST International Journal of Research, Vol2, Issue II, July 2014 page 51-57.
3. Shikhar Gupta, Prashnat Kumar, Anjali Chawla, "Comparative analysis of fully Depleted DMGSOI MOSFET SMG SOI MOSFET" International Journal of Recent Research Aspects, Vol 2 Issue 2, June 2015 page 125-128.

International/National Conferences Attended:

1. Prashant Kumar, Khusboo Sagar, Nisha Goel, "Design & comparison of Booth's & Robertson's Algorithms multiplier on various parameter" International conference on VLSI, MEMS & NEMS held on 24-25 Jan 2012 at AMITY University, NOIDA, Uttar Pradesh.
2. Prashant Kumar, Neeraj Gupta "Linearity based Comparison of CMOS VCOs" 1st International Conference on Advanced Computing & Communication Technology held on 20-22 Jan, 2011 at RG Education Society, Rohtak, Haryana, INDIA.
3. Prashant Kumar, Pankaj Negi, "Analytical Comparison of Different 1-Bit full adder's scheme for 250nm CMOS Technology", International conference on Biomedical Engineering and Assistive Technology held on December 17-19, 2010 at Dr. B R Ambedkar National Institute of Technology, Jalandhar, Punjab, INDIA.
4. Prashant Kumar, Neeraj Gupta, Dr. K.S. Yadav "Comparative Study of Various Ring Oscillator Based CMOS VCOs" International Conference on Innovative Technology and Management Research and Development in Science, Technology and Management (ICIT- 09) held on June 18-19, 2009 at PDM college of Engineering, Bahadurgarh, Haryana, INDIA.

National Conference:

1. Munish Vashishath, Prashant kumar, Khushboo, Shireesh "Implementation of Sub Domino AND Gate" 1st National Conference "NCNPECC-2012" on May 19, 2012 at ECHELON Institute of Engineering, Faridabad.
2. Prashant Kumar, Nidhi Sharma, Ritu Sharma "Different Adiabatic Technique for Power Reduction" 1st National Conference "NCNPECC-2012" on May 19, 2012 at ECHELON Institute of Engineering, Faridabad.
3. Prashant Kumar, Nidhi Sharma, "Conventional CMOS Logic Versus ADIABATIC Logic Circuit for Power Reduction" 2nd National conference on Emerging trends in Electronics & Information Technology held on 14-15 April, 2012 at AITM Palwal, Haryana, INDIA.
4. Prashant kumar, Khushboo and Nisha, "Charge Restoring and Leakage Reduction Techniques in Domino Circuit", 2nd National conference on Emerging trends in Electronics & Information Technology held on 14-15 April, 2012 at AITM Palwal, Haryana, INDIA.
5. Prashant Kumar, Pankaj Negi "Analysis & Comparison of Different 1 Bit Full adders for various Parameter" 2nd National conference on Electronics Design & Communication Technology held on 14-15 May, 2010 at Haryana College of Technology & Management, Kaithal, Haryana.
6. Prashant Kumar, Prashant Yadav and Suman Dahiya' "Comparison and analysis of different differential amplifiers structures" Conference on Technology for Rural India: Challenges & Perspective held on May 31 and June 1, 2008 at NSIT, New Delhi.

7. Prashant Kumar, Manoj K. Taleja, "Techniques for Frequency compensation of a Two stage operational Amplifier" National Conference on Emerging Trends in Engineering & Technology (NCETET-08) held on 26-27 May 2008 at DCRUST, Murthal, Haryana.
8. Prashant Kumar, Neeraj Gupta, "A Low Power Minimum Transistor CMOS XOR-XNOR Full Adder" National Conference On Recent Trends in Electronics & Communication held on 10-11 April 2008 at SUSCET, Tangori, Mohali, Punjab.
9. Prashant Kumar, Neeraj Gupta, "Suggestive Techniques for Protection of ICCircuit from ESD" National level Technical Paper Presentation on Evolution Towards Next Generation Technologies held on 05 April 08 at Deptt. Of ECE, NIT Kurukshetra, Haryana.
10. Shikhar Gupta, Prashant Kumar, Munish Vashishath "Parameters Extraction of 200nm NMOS Transistor using Silvaco TCAD Tools" Second National Conference on Machine Intelligence and Research Advancement (NCMIRA-2015) held on at BPS Khanpur Kalan Sonipat 19-20 March 2015.
11. Prashant Kumar "Analytical comparison of Different 1- bit full adder scheme for 250nm CMOS technology" National conference on Science in Media (SIM 2012) held on 3-4 Dec 2012 at YMCAUST, Faridabad, Haryana.
12. Prashant Kumar "OP-AMP simulation and measurement for various parameters" Symposium on Nanotechnology: Interdisciplinary Aspects held on 12 Dec. 2012 atYMCAUST, Faridabad, Haryana.

Short Term Courses/ Workshops/Seminar Attended:

1. One Week STTP on Low Power System On chip (SoC)-VLSI Design: Techniques, Tools and Technology-08(LPSVDT)at ABV-Indian Institute of Information Technology & Management, Gwalior from 15-22 Dec, 2008.
2. One Week STC on VLSI Design and Optimization Techniques (VDOT-2009)at NIT Hamirpur from 06-10 July, 2009.
3. One Week STC on Engineering Application of EDA Tools using Verilog & SPICE (EDAT-09) at NIT Hamirpur from 13-17 July, 2009.
4. One week FDP on Advanced Microprocessors and Microcontrollers (AMM-2010) at NIT Calicut from 12-17 July, 2010
5. One Week FDP on Instrumentation & Signal processing at Dr.BRA NIT Jalandhar from 11-15 Jan, 2010.
6. One week STC on Application of MATLAB at SVNIT Surat from 22-27 Dec 2009.

7. Two Weeks STC on Optical fibers: Communication & Sensing Applications at NITTTR Chandigarh from 16-27 July, 2012.
8. Two weeks STC on Analog Electronics at AMITY Gurgaon from 04-14 June, 2013
9. One week STC on Emerging Materials: Characterization and Applications at NIT Kurukshetra from 13-17 June, 2014.
10. One week STC on Synthesis and characterization techniques of smart materials at NIT Kurukshetra from 22-26 June 2014.
11. One week STC on Effective Teaching Methods at YMCA Faridabad from 04-08 June, 2015.
12. Three days STTP on MATLAB Fundamentals & Applications at GBU Gr. NOIDA from 13 - 15 July 2011.
13. Two Days workshop on VLSI TCAD for Device Research and Development at AIT, New Delhi from 02-03 Aug, 10.
14. Three Day workshop on Cyber& Information Security at YMCA Faridabad from 25-27 October 2013.