Rashmi Chawla
Assistant Professor
YMCAUST,Fbd.
M. Tech (Digital Communication)
PG Diploma in VLSI Design
BE (Electronics & Telecommunications)

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**Professional Goal****: -** To continue Career Growth by accepting challenging responsibilities and keen to undertake higher studies for career progress.

**Areas of Interest:**

- VLSI Designing
- Embedded systems
- Digital system design ,Simulation Lab,Matlab

**Work Experience**:-
Oct'08 – till date
   Working as Assistant Professor at YMCA University of Science and Tehnology– Faridabad

Jan'05 – Oct'08
   Worked as Lecturer at CITM – Faridabad

Apr'03 – Dec'04
   Visiting Faculty in YMCA – Faridabad

(Feb'02 –Dec'02)
   Lecturer on Ad hoc at Maulana Azad College of Technology (MANIT)-REC-Bhopal
   .

**Professional Qualification:**   **M. Tech (Digital Communication Aggr: 78%)**

                                             PG Diploma in VLSI Design. C-DAC, Nagpur (2001-2002)

                                             BE (Electronics & Telecommunications) from R.G.P.V.V. University, HCET Jabalpur (MP) (Aggr: 74.38%) (1997-2001)

**Projects Undertaken**

- Design of Standard Functions and TOFFOLI Gate and Reversible Comparator Using QCA

- Image Restoration by Fog Removal Technique

- Low power clocked Pseudo NMOS flip flop-T-Spice of Tanner EDA tool v14.1

- Fault Tolerant ALU designed using Parity Preserving Reversible Gates

- **Project - PCI 32 bit/33 MHz. Target Interface**

- ModelSim HDL Simulator, Leonardo Spectrum Synthesis Tool and XilinxISE 4.1 Back End Tool.

- **Project- 8259 Interrupt Controller**

Project Synopsis
- Upto 64 levels of Interrupts.
- Master and Slave Operation.
- Complete Design according to Intel 8259A Interrupt Controller.

Tools Used:
- ModelSim HDL Simulator, Leonardo Spectrum Synthesis Tool and Xilinx ISE 4.1 Back End Tool.

- **Project--Telephone Based Integrated Ecommerce System**

Doing research in Embedded system application in Energy Management.
Guided many students project out which 3 won prize in FIA (Faridabad industry association)

Conferences/Seminars   :

Training Program on Science technology and Emerging trends in Governance organized by IIPA Delhi	18~22 Feb'13
Core teaching skills organized by NITTTR, Bhopal	10~21 Jun'13
Workshop on NANO Technology on embedded system	23 Jul~3rd Aug'12
FDP on Information Security and Cyber Forensic	9~14th Jul'12
Microcontroller organized by NITTTR, Bhopal	25~29 Jun'12
Management Capacity Development Training at YMCA Institute of Engineering	2~6 Mar'09
Staff development program at LIMAT on Emerging trends on Signal Processing and Industrial Drives.	10~21 Jul'06

Paper Published

AN ADAPTIVE PSEUDORANDOM STEGO-CRYPTO TECHNIQUE FOR DATA COMMUNICATION	International Journal of Computer Networks & Communications	2013
A 5/1 Algorithm **Based on Quantum-DotCellular Automata**	**International Journal of P2P Network Trends &**	March 2014

(QCA)Technology .	Technology (IJPTT)	
Combinational Circuit Design using Advanced Quantum Dot Cellular Automata	RACE-2014	2014
DESİGN AND ANALYSİS OF FULL ADDER CİRCUİT USİNG NANOTECHNOLOGY BASED QUANTUM DOT CELLULAR AUTOMATA (QCA)	ICDAVIM-15 ISBN	2015
Fault Tolerant Digital System Design using Reverse Gate 23July~3rd Aug'12	YMCA University	2012
Design of a Half Swing Clock Driver for large power saving in clocking circuitory , 3~4th dec'12	National Conference	2012
Reversible Gates and their use in designing a comparator	National Conference	2012
Study of Reduced Clock Swing Flip Flop, D Flip Flop with MTCMOS Technique and Comparison with Conventional D Flip Flop 23~24 Jun'12	International Conference	2012
Solution of connectivity challenges in mobile design using Embedded system	National Seminar	2007

Computer Proficiency:

- Embedded softwares (KEIL, Flash Magic)
- DSP Software (MATLAB)
- DSD Software (VHDL,Verilog)
- Simulations Software (PSPICE)

Date: 4 - Jul-2013
Place: Faridabad   **(Rashmi Chawla)**