

Activity Report

Name of the Activity: Value Added Course in “LTspice Circuit Simulation and Analysis Fundamentals” Oct-Nov 2023

Name of department/ Section/ cell conducting the activity	Department of Electrical Engineering
In coordination with (if any)	NA
Date of Conduct	From 27 th October
Activity Coordinator	Mr Nitin Goel and Dr Shakuntla, Assistant Professor, Department of Electrical Engg. JCBUST, YMCA, Faridabad
Amount Spent	NIL
Funding/ grant from (University/ Industry/ UGC/ AICTE/ DST/ TEQIP/ Outside Society/ agency/others (mention)	NIL
Target audience:	Undergraduate Students
No. of beneficiaries	67
Details of Experts	Mr. Nitin Goel and Dr. Shakuntla Assistant Professor, Department of Electrical Engg. JCBUST, YMCA, Faridabad
Brief Description of the event	This course is designed for beginners who want to learn how to use LTspice, a popular electronic circuit simulation software. Students will learn the fundamentals of circuit simulation, how to create and analyze electronic circuits and gain practical experience through hands-on exercises.

Attach a Brochure of the event



J. C. Bose University of Science and Technology, YMCA, Faridabad

Value Added Course
2023-24

LTSpice Circuit Simulation and Analysis Fundamentals

Offered by
Department of Electrical Engineering



Key Features:

- Learn the basics, installation, and workspace familiarity.
- Hands-on circuit analysis: DC, AC, and Transient.
- Explore semiconductor devices and amplifier circuits.

Chairperson

Dr. Anju Gupta
Electrical Engineering
JCBoseUST, YMCA

Coordinator and Resource Peron

Nitin Goel & Dr. Shakuntla
Assistant Professor
Electrical Engineering

The certificate will be provided on successful completion of the course

Registration Link:

https://docs.google.com/forms/d/e/1FAIpQLSehrmmct5V_MCn4a0q_wY7u-KZfAVJxA_OqvKOaLO4QmGfD3g/viewform?usp=sf_link

Attach two/ three good-quality photographs



Attach certificate of the event

J. C. Bose University of Science & Technology, YMCA, Faridabad (HR)
(Accredited A+ Grade by NAAC)



CERTIFICATE OF COMPLETION
Value Added Course in
LTspice Circuit Simulation and Analysis Fundamentals

This is to certify that Mr./Ms. «Student Name» Roll No. «Roll Number» of **B.Tech 3rd Semester Electrical Engineering** has successfully completed the **Value Added Course** in "**LTspice Circuit Simulation and Analysis Fundamentals**" conducted by the Department of Electrical Engineering, J. C. Bose University of Science and Technology, YMCA, Faridabad during Oct-Nov 2023.

NITIN GOEL
COORDINATOR

DR. SHAKUNTLA
COORDINATOR

CHAIRPERSON
ELECTRICAL ENGG.

Any other information

NA