



Department of Electronics Engineering

Ref. No. HSCSIT/R&D/2025/1546/01

Date: 14.11.2025

Applications are invited for the post of **Research Assistant (Temporary)** to work in the Haryana State Council for Science Innovation and Technology, funded R&D project titled "**Smart Photovoltaic Thermal (PVT) Bench with Charging Station for EVs**" has been sanctioned to **Dr. Rohit Tripathi (PI)**, Dept. of Electronics Engineering and **Dr. Nikhil Dev**, Dept. of Mechanical Engineering at J C Bose University of Science and Technology, YMCA Faridabad, Haryana.

| | |
|--------------------------------|---|
| Name of Post | Research Assistant (One Post) |
| Consolidated emoluments | INR (25K+HRA@16%) 29000 per month |
| Status of Job | Temporary |
| Duration | 3 years or completion of project, whichever is earlier |
| Place of work | J C Bose University of Science and Technology, YMCA, Faridabad, Haryana |

Minimum qualification: M. Tech in Electronics and Communication Engineering/Electronics Engineering with minimum 55% marks or equivalent CGPA.

Essential Qualifications: NET/GATE in concerned subject.

Desirable Qualifications:

1. Research experience in specific areas like IoT, Renewable Energy Systems, Machine Learning, Power Electronics, Embedded Systems.
2. Good knowledge of programming, simulation tools, and experimental techniques.
3. Strong communication and technical or scientific report-writing skills.

Process of Selection: The selection will be made on the basis of interview. Interested candidates are requested to send their CV latest by 15 December 2025 to e-mail address: rohittripathi30.iitd@jcboseust.ac.in. Applicants must bring their testimonials/documents in original and one self-attested copy for interview.

Date and venue of walk-in-interview: On 10:00 AM, 23 December 2025 in the department of Electronics Engineering, J C Bose University of Science and Technology YMCA, Faridabad, Haryana.

Terms and Conditions:

1. *The post is purely temporary and is coterminous with the project. Fellowship is liable to cancellation in case of: a) misconduct, b) unsatisfactory research work, and c) fellow is found ineligible later.*
2. *No TA/DA shall be paid to the candidate for attending the interview.*
3. *The selected candidate will have to work in the project mentioned above or any other work related to the project that may be assigned by PI from time to time.*
4. *Written test in subject area will be held. If required, to shortlist the candidates for interview on the same day depending upon the number of candidates.*
5. *Selected candidates will not have any right to claim for regular appointment on the basis of this contract appointment.*



**Dr. Rohit Tripathi
Principal Investigator (PI)**

Department of Electronics Engineering
J C Bose University of Science and Technology, YMCA, Faridabad, Haryana