YMCA University of Science and Technology, Faridabad

(A Haryana State Government University)

(Established by Haryana State Legislative Act No. 21 of 2009 & Recognized by UGC Act 1956 u/s 22 to Confer Degrees)

OFFICE OF CONTROLLER OF EXAMINATIONS

Ref. No. COE/Exam/D18/15

Dated: - 3rd DEC. 2018

Date Sheet of Theory Papers

<u>Credit Base System</u> <u>Scheme-2010</u> B. Tech 5th Semester Reappear Examination- DEC. 2018

Time of examination

: 2.00 PM TO 5.00 PM (EVENING)

DATE	BRAN CH	SUBJECT CODE	FULL NOMENCLATURE
11-12-2018	IT/CE	CE- 301	PRINCIPLE OF OPERATING SYSTEM
	EL	EL-301	ELECTRICAL MACHINES-III
	ECE	ECE-301	COMMUNICATION ENGNEERING.
	EIC	EIC-301	TRANSDUCERS & SIGNAL CONDITIONING
13-12-2018	IT/CE	CE-303	INTERNET FUNDAMENTALS & WEB TECHNOLOGY
	EL	EL-303	PRINCIPAL OF COMMUNICATION ENGNEERING.
	EIC	EIC-303	NON LINEAR CONTROL SYSTEM
15-12-2018	IT	IT -305	MULTIMEDIA TECHNOLOGY
	CE	CE -305	COMPUTER GRAPHICS AND MULTIMEDIA TECHNOLOGY
	М	MU -305	INTERNAL COMBUSTION ENGINES
	EL/ECE/EIC	E -305	ANALOG INTERGRATED CIRCUITS
18-12-2018	IT	IT- 311	NETWORK SECURITY
	CE	CE- 307	ANALYSIS & DESIGN OF ALGORITHMS
	EL	EL- 307	POWER SYSTEM – I
	ECE	ECE- 307	ANTENA & WAVE PROPAGATION
	EIC	CE-203	DATA STRUCTURE
20-12-2018	IT/CE	CE -309	WIRELESS COMMUNICATION
	М	MU- 309	MACHINE DESIGN - I
	EL/ECE/EIC	E- 309	POWER ELECTRONICS
22-12-2018	IT	IT -307	NETWORK PROGRAMING & ADMINISTRATION
	CE	CE -311	SYSTEM SOFTWARE DESIGN
	M	MU- 311	NUMERICAL ANALYSIS & COMPUTER PROGRAMING
	EL/ECE/EIC	E- 311	MICROPROCESSORS & INTERFACING

Important Instructions

- 1. Books, any other Printed, Handwritten, Course Material etc, Bags, Mobile Phones, Programmable Calculators, Tablets & Other Electronic Gadgets are not permitted inside the premises of the Examination Centre, **if any candidate found with any such material, candidate may be booked under unfair means case.**
- 2. No candidate will be allowed to enter the Examination Hall without University I-card and admit card.





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