



Department of Electronics Engineering
Notice

Ref: JCBUST/EE/23/5480

Date:- 13-07-2023

Sub: Ph.D. Viva Voce of Ms. Sangeeta Dhall (Regn No. 17-YMCA-904010).

With reference to office order no JCBUST/EE/23/5464 dated 28-06-2023 there is partial modification. The Final Ph.D. viva voce of **Ms. Sangeeta Dhall (Regn No. 17-YMCA-904010)** Ph.D. Scholar on the topic "**Analysis and Design of a Multilevel Security Mechanism for Data Communication Networks**" is re-scheduled to be held on 28/07/2023 at 11:00 am in offline mode in Conference Hall of Electronics Engineering Department. All the Faculty members, Ph.D. scholars & PG students are invited to attend the Ph.D. viva voce of Ms. Sangeeta Dhall, Research Scholar.

13/07/2023

Chairperson
(Electronics Engineering)

CC:

1. PA to VC for kind information
2. PA to Registrar for kind information
3. Dean of Institutions
4. Director R&D
5. Dean Academics
6. Dean FoET
7. Chairperson CE/ME/CA/MBA/IT/EL/Phy./Chemistry/Eng./Maths/Civil/EVS/JMC
8. Ms. Sangeeta Dhall, Research Scholar
9. Dr. Shailender, Supervisor
10. COE
11. COF
12. Ph.D. Coordinator
13. Mr. Ankit Pannu, Web-Manager, (Please upload on University Website)
14. Examiner