

Name	Dr. Munish Vashishath	
Designation	Professor and Chairperson	
Date of Joining	06-07-2004 as Lecturer 04-01-2007 as Associate Professor 04-01-2013 as Professor	
Qualification	B.E (Electronics & Telecommunication) M.Tech(Electronics & Control) Ph.D. (Semiconductor Devices)	
Area of Specialization	Communication and Semiconductor Devices	
Teaching Experience	17 Years	
Contact: E-mail / Phone	munishvashishath@ymcaust.ac.in munish.vashishath@gmail.com	

THESIS/ DISSERTATION/ PROJECT SUPERVISION EXPERIENCE

Ph.D (07 Supervising)

M.Tech (29 Supervised+03 in progress)

MEMBERSHIP IN PROFESSIONAL ASSOCIATIONS

IEEE

PUBLICATIONS

International/National Journals:

1. A.K.Chatterjee & Munish Vashishath, "Theoretical Analysis And Design Of Double Implanted MOSFET On 6H Silicon Carbide Wafer For Low Power Dissipation And Large Breakdown Voltage", Maejo International Journal of Science & Technology, vol.2, no.2, pp. 308-319, 2008 (Impact Factor=0.433).
2. A.K.Chatterjee & Munish Vashishath, "Recent Trends In Silicon Carbide Device Research", Maejo International Journal of Science & Technology, vol.2, no.3, pp. 444-470, 2008 (Impact Factor=0.433).
3. A.K.Chatterjee & Munish Vashishath, "Recent Advances in Silicon Carbide Device Based Power MOSFETs", Journal of Electrical Engineering, vol.9, no.1, pp.21-32, 2009.
4. A.K.Chatterjee & Munish Vashishath, "Evaluation of Substrate and well doping Levels and Chip Dimensions to Prevent Latch-Up in CMOS Inverter Circuits in Silicon using p-well Technology with Line Geometry of 0.5 μm ", ARPN Journal of Engineering and Applied Sciences, vol.4, no.3, pp. 95-102, 2009.
5. A.K.Chatterjee & Munish Vashishath, "Analytical Estimation of Breakdown Voltage and Power Dissipation of Double Implanted MOSFET on 6H Silicon Carbide Wafer with Linearly Graded Profile", Journal of Electron Devices, vol.7, pp. 214-224,2010.

6. Munish Vashishath &, Kuldeepak Kaushik, “ License Plate Recognition System based on Image Processing using Labview”, International Journal of Electronics Communication and Computer Technology (IJECCT) Volume 2 Issue 4, pp.183-188, 2012 (Impact Factor=01)
7. Sunil Jadav & Munish Vashishath “Design And Performance Analysis Of Ultra Low Power 6T SRAM Using Adiabatic Technique”, International Journal of VLSI Design and Communication System (VLSICS) Vol.3, no.3, pp. No- 95-105, June 2012 (Impact Factor=01).
8. Sunil Jadav, Munish Vashishath & Rajeevan Chandel “Study and Performance Analysis of Two Stage High Speed Operational Amplifier Using Indirect Compensation”, International Journal Engineering Science and Technology, ISSN: 2250-3498, Vol.2, No. 4, pp- 840-846, August 2012.
9. Munish Vashishath & Nitin Sachdeva, “Leakage Reduction in various logic Circuits using VTCMOS”, International Journal on Advanced and Innovative Research, vol.2, issue2, 2013 (Impact Factor=01.770)
10. Munish Vashishath & Ashok Kumar, “Performance Comparison between various Tuning Strategies: Ciancone, Cohen Coon & Ziegler- Nicholas Tuning Methods, International Journal of Computers & Technology, volume 5, no. 1,pp 60-68, May - June, 2013, ISSN 2277-3061 (Impact Factor=2.340)
11. Munish Vashishath & Arun Kumar, “Liquid Level Control of Coupled tank system using Fractional PID controller”, International Journal of Emerging Trends in Electrical and Electronics, volume 3, issue1, 2013, ISSN 2320-9569.
12. Munish Vashishath, Vasudha Sharma and S.K.Gupta, “Analysis of Power Dissipation of 6H Silicon Carbide Double Implanted MOSFET with Linearly Graded and Uniformly Doped Profile at Breakdown Voltage of 10kV”, ISSN 2320-6802 International Journal for Advance Research in Engineering & Technology, vol.1,issue IV, pp.73-79, May,2013.
13. Munish Vashishath, Pinki Rani and Prashant Kumar, “An Efficient Approach to Design 5 Bit Flash ADC 50”, YMCAUST International Journal of Research, vol.1, issue2, pp. 50-58, July 2013.
14. Munish Vashishath & Sunil Jadav, “Design and Validation of threshold Model Using BSIM3v 3.2.2”, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, vol. 2, issue 10, pp. 4773-4780, October 2013 (Impact Factor=1.686) .
15. Sunil Jadav and Munish Vashishath, “Current Mode Signalling for Global VLSI Interconnect : A Review”, YMCAUST International Journal of Research, vol.2, issue1, pp. 32-35, January 2014.
16. Dushyant Shukla and Munish Vashishath, “Noise Analysis of Common-Emitter Amplifier using Stochastic Differential Equation”, International Journal of Advanced Research in Computer and Communication Engineering”, vol.3, issue3, March,2014 (Impact Factor=1.770)
17. Dushyant Shukla and Munish Vashishath, “Noise Analysis of Common-Collector Amplifier using Stochastic Differential Equation” International Journal of Engineering and Advanced Technology (IJEAT), vol.3, issue 4, April, 2014 (Impact Factor=1.097)
18. Shubham Tayal, Sunil Jadav and Munish Vashishath, “Implementation Of Logic Gates Using Charge Recycling MTCMOS Technology”, International Journal of VLSI and Embedded Systems-IJVES, vol 05, pp 1008-1010, May 2014 (Impact Factor=1.89).

19. Dushyant Shukla and Munish Vashishath, "Noise Analysis of Common-Base Amplifier using Stochastic Differential Equation", International Journal of Electronics Communication and Computer Engineering, vol.5, issue 2, pp 381-383, 2014 (Impact Factor=1.0909).
20. Sunil Jadav Munish Vashistah Rajeevan chandel "Carbon Nanotube Based Delay Model For High Speed Energy Efficient on Chip Data Transmission Using: Current Mode Technique" has been published, Electrical and Electronics Engineering: An International Journal (ELELIJ) Vol 3, No 4, November 2014.
21. Rohit Rohila and Munish Vashishath, "CMOS LECTOR Technique: A Technique for Leakage Reduction in Domino Circuits", International Journal for Scientific Research & Development, vol. 3, Issue 03, pp 2279-2281,2015.
22. Krishan Kumar and Munish Vashishath, "Self-Bias Transistor & Transmission Gate Logic Technique: for 8:1 Multiplexer", International Journal for Scientific Research & Development, vol. 3, issue 04, pp 1632-1634, 2015.
23. Sunil jadav Munish Vashistah and Rajeevan Chandel "A Superior Delay Estimation Model for VLSI Interconnect in Current Mode Signaling", International Journal of Computer and Electronics Engineering (World Academy of Science), in Vol:9, No:2, 2015.
24. Sunil Jadav Munish Vashistah and Rajeevan Chandel " RLC Equivalent RC Delay Model For Global VLSI Interconnect In Current Mode Signalling", Journal of Modelling and Simulation(Taylor and Francis Journal) in Aug 2015.

International / National Conferences Attended:

1. Munish Vashishath, Ashwani Rana & Bharat Bhushan, " Digital Optical Fiber Link Design Calculations in Optical Communication, National Seminar on Emerging Trends in Convergent Technologies, DETI-Agra, March 2-3, 2002.
2. Munish Vashishath, Ashwani Rana & Surender Soni, "Automatic Regulations in Power Line Problems in Industrial Enviornment", International Conference on DAMS-2003, Coimbatore College of Engineering and Technology, Coimbatore, July 4-6, 2003.
3. A.K.Chatterjee & Munish Vashishath, "Performance of Silicon Carbide Wafers for Semiconductor Power Devices", National conference of Materials and Related Technologies, TIET-Patiala, September19-20, 2003.
4. A.K.Chatterjee & Munish Vashishath, "Mathematical Modeling and Analysis of various DC-DC Converter Basics, Trends in Instrumentation and Control Engineering, TIET-Patiala, February 5-6,2004.
5. A.K.Chatterjee & Munish Vashishath, "Innovative Considerations for Robust Design of SCR and TRIAC Power Conditioning Circuits, Trends in Instrumentation and Control Engineering, TIET-Patiala, February5-6,2004.
6. Munish Vashishath and A.K.Chatterjee, "Optimum Doping Level of Drift Region of 10 KV 6H- SiC Power DIMOSFET For the Power Dissipation", International Conference on Resource Utilization and Intelligent Systems (INCRUIS-2006), Kongu Engineering College- Erode, Tamil Nadu, January 4-5,2006.
7. Munish Vashishath and A.K.Chatterjee, "Mathematical Analysis and Design of Power DIMOSFET on 6H-SiC Wafer", National Conference on "Innovations 2005", BBSBEC-Fathehgarh Sahib from Nov.25-26,2005.
8. Munish Vashishath and A.K.Chatterjee, "Field Dependent Mobility Analysis of High Voltage SiC DIMOSFET using Graded Doping in the Drift Region", Second National

- Conference On Electronic Circuits & Communication Systems (ECCS-2006) in TIET, Patiala on 09-10 February 2006.
9. Munish Vashishath and A.K.Chatterjee, "Field Independent Mobility Analysis of High Voltage SiC DIMOSFET using Graded Doping in the Drift Region", National Conference On Circuits , Computer & Communication Systems (NCCCB-2006) in Engineering College-Kota on 09-10 March 2006
 10. Munish Vashishath and A.K.Chatterjee, " Design of 6H SiC DIMOSFET for Low Power dissipation/Analysis & Design of 6H-SiC Double Implanted MOSFET and Optimize Doping Level In Drift Region & Power Dissipation", IEEE International Conference on Computer & Communication Engineering in Kuala Lumpur, Malaysia, on 09-11May 2006 .
 11. Munish Vashishath and A.K.Chatterjee, "Estimation of Effective Doping level of a semiconductor having a Gaussian Profile", National Conference on "Communications 2006", CIET-Banur on 27-28 July 2006.
 12. Munish Vashishath and A.K.Chatterjee, "Design of a DIMOSFET on 6H-SiC Wafer using Field Dependent Mobility", National Conference on "Communications 2006", CIET-Banur on 27-28 July 2006.
 13. Munish Vashishath and A.K.Chatterjee, "Design of Double Implanted Power MOSFET for Large Breakdown Voltage on 6H SiC Wafer", National conference on "DTVC-2007", NIT-Hamirpur on 14-15 May 2007.
 14. Munish Vashishath, Sakhi Bangia and Vijay Kumar, "Design of Power Stabilizer using Single Input Fuzzy Logic Controller", National conference on "DTVC-2007", NIT-Hamirpur on 14-15 May 2007.
 15. Munish Vashishath & Shaileza Agarwal, "Performance Analysis of 6H-SiC DIMOSFET using Gaussian Doping Profile", National Conference on -2008, P.D.M.C, Bahdurgarh.
 16. Vikrant, Sunil Jadav, Munish Vashistah and Rajeevan Chandel "Sub-threshold Leakage reduction of 6T SRAM cell using optimum bulk bias", National Conference on Science & Technology, S. B. Patil College of Engineering, Indapur- Pune (Maharashtra), Jan.2012.
 17. Munish Vashishath and Kuldeepak Kaushik, "License Plate Detection and Recognition Using Wavelets and Back Propagation Neural Network", 1st International Conference and 5th CSI National Conference on Education & Research, Lingaya's University, Faridabad, pp 95-98, 2012.
 18. Jitender Kumar, Munish Vashishath and Nitin Sachdeva, "Leakage Power Reduction in CMOS Logic Circuits by Transistor Stacking Technique", Proceedings of National Conference on New Paradigms in Electronics, Communication and Computing, Echlon Institute of Technology, Faridabad, pp.63-66,May, 2012.
 19. Munish Vashishath and Prashant Kumar, "Implementation of Sub threshold Domino AND Gate", Proceedings of National Conference on New Paradigms in Electronics, Communication and Computing, Echlon Institute of Technology, Faridabad, pp.114-117, May, 2012.
 20. Munish Vashishath and Nitin Sachdeva, "COMPARATIVE STUDY OF CMOS INVERTER USING DIFFERENT LOGIC STRUCTURES", Workshop on Nanotechnology & Embedded System, YMCA UST-Faridabad, 2012.
 21. Munish Vashishath and Kuldeepak Kaushik, "License Plate Recognition System based on Vision Assistant Using Labview", Workshop on Nanotechnology & Embedded System, YMCA UST-Faridabad, 2012.

22. Munish Vashishath and Ashok Kumar, "Level Control of A Three Tank Non-Interacting System using Intelligent Controller", Workshop on Nanotechnology & Embedded System, YMCA UST-Faridabad, 2012.
23. Sunil Jadav and Munish Vashishath, "Resistance Calculation for CNT Bundle for Global VLSI Interconnect", Workshop on Nanotechnology & Embedded System, YMCA UST-Faridabad, 2012.
24. Munish Vashisath and Ruchika Rani, "Design of IIR Band Pass Filter using Time Domain Approach", National Conference on Trends and Advances in Mechanical Engineering, YMCA UST-Faridabad, 2012.
25. Munish Vashishath and Prashant Kumar, "Analytical Comparison of Different 1-Bit full Adder Scheme for 1-bit full adder scheme for 250 nm CMOS Technology, National conference on Science in Media, YMCA UST-Faridabad, 2012.
26. Munish Vashishath and Prashant Kumar, "OP-AMP: Simulation and Measurement for various parameter", Symposium on Nanotechnology: Interdisciplinary Aspects (SONIA-2012), YMCA UST-Faridabad, 2012.
27. Vikrant, Sunil Jadav, Munish Vashistah, "Sub-threshold leakage reduction of 6T SRAM Cell using Optimum Body bias" National Conference on Recent Developments in Electronics (NCRDE-2013), Department of Electronic Science, University of Delhi South Campus, 2013.
28. Sunil Jadav Munish Vashistah Rajeevan chandel "Close form delay model for on chip signalling with resistive load termination using: Current mode technique" has been published in IEEE proceeding of 9th international conference on industrial and information system, organized by IIIT Gwalior on 15-17th Dec 2014.

Short Term Courses/ Workshops/Seminar Attended:

Sr. No.	Title	Organized by	Session
1.	National workshop on Thin Film Materials & Devices (NWTMD-05)	National Institute of Technology Karnataka, Surathkal	September 23-24, 2005
2.	VLSI Design & Optimization Techniques (VDOT-2006)	National Institute of Technology, Hamirpur, Himachal Pradesh	June 05-09, 2006
3.	AICTE Sponsored Induction Training Programme for Newly Recruited Technical Teachers	YMCA Institute of Engineering, Faridabad conducted by EDU, & EDCL. MGT Department of NITTTR, Chandigarh	July 17-28, 2006
4.	Curriculum Development Implementation	YMCA Institute of Engineering, Faridabad conducted by Curriculum Development Centre of NITTTR, Chandigarh	September 04-09, 2006
5.	Design and Conduct of Tracer Studies for Pass-Outs	Information Management & Coordination Unit of NITTTR, Chandigarh	November 08-10, 2006
6.	DSP Tools & Practice	G.S.Sanyal School of Telecommunications, IIT-Kharagpur	June 04-09, 2007
7.	Two Weeks Programme on Pedagogical Training in	Engineering Staff College of India, Hyderabad	December 01-13, 2008

	Engineering Education		
8.	Managerial Skills	Conducted by Centre for Organizational Research & Development New Delhi on behalf of SPFU, Haryana, under TEQIP at Panchkula	April 22-24, 2009
9.	Recent Trends in Digital Signal Processing	Atal Bijhatri Vajpayee Indian Institute of Information Technology & Management, Gwalior	December 07-11, 2009
10.	VLSI TCAD for Device Research and Development	Ambedkar Institute of Technology, New Delhi in Collaboration with Synopsys, India & assisted on Lectures by Dr. Anil Kottantharayil, Associate Professor, Deptt. of Electrical Engg., IIT Bombay	March 12-13, 2011
11.	MATLAB Fundamentals & Applications	Gautam Buddha University, Greater Noida	July 13-15, 2011
12.	Application of Artificial Intelligence Tools in Power system	YMCA University of Science and Technology, Faridabad	June 19-25, 2012
13.	Information security and Cyber Forensic	YMCA University of Science and Technology, Faridabad	July 09-14, 2012
14.	Workshop on Nanotechnology & Embedded System	YMCA University of Science and Technology, Faridabad	23 rd July-3 rd August, 2012
15.	Two Days Workshops on “Program Educational Objectives and Outcomes”	Institute of Engineering & Technology, Alwar	September 14-15, 2013
16.	One Week Short term Training Program on “Applications of Artificial Intelligence Techniques in Engineering and Research	Birla Institute of Technology, Mesra Ranchi	September 23-27, 2013
17.	One Week Faculty Development Program on Teaching Skills Enhancement	YMCA University of Science and Technology, Faridabad	January 06-10, 2014

List of Invited talks Delivered

S.No.	Topic	Place	Date
1.	Analytical Analysis of DOUBLE IMPLANTED MOSFET by using different profiles	Applied College of Management and Engineering, Palwal	Feb. 24-25, 2011
2.	Expert Lect. On Microwave & DSP	Appejay Satya University, Sohna	Feb. 2011
3.	Expert Lect. On Microwave & DSP	Appejay Satya University, Sohna	March 2011

4.	Expert Lect. On Microwave & DSP	Apperjay Satya University, Sohna	April 2011
5.	Key note address on Analysis of DIMOSFET by using Different Profiles	IET, Bhaddal, Ropar	2 November, 2011
6.	Trends in Microprocessor & Microcontroller	YMCA UST, Faridabad	23 July – 4 August 2012
7.	Trends in Microprocessor & Microcontroller	Delhi College of Technology & Management	25 & 26 May-2013
8.	Emerging Trends in Engineering, Technology and Management	YMCA UST, Faridabad	1 July 2013- 14 July 2013
9.	A Step to NBA Accreditation: Program Educational Objectives and Outcomes	Chandigarh Engineering College, Landran	Feb. 08-09, 2014